

Amendments to the Drawings

The attached sheet of drawings includes changes to Figs. 7-8b. This replacement sheet includes Figs. 7-8b, is to be substituted for the original sheet including Figs. 7-8b.

In Fig. 7, an arrow from the connecting node of the output terminal of the Feedback Loop block (for outputting the VE/A signal) and the resistor (Re) to the Ramp Generator block has been added. Also, for clarity, in Fig. 7, the Feedback Loop, Ramp Generator, and PWM Comparator are changed to be shown in phantom.

In Figs. 8a and 8b, the time correlation lines are changed to be shown in phantom.

REMARKS/ARGUMENTS

A replacement sheet of drawings for Figs. 7, 8a and 8b is submitted herewith. Clarification has been made, but no new matter has been added. Approval of the substitution of this sheet for the original sheet of drawings is respectfully requested.

Claims 1-19 are currently pending in this application. In their March 31, 2005 Reply to the March 3, 2005 restriction requirement, Applicants elected Species II, represented by Figure 7, read upon by claims 5-9 and 14-19. Therefore, in the present Action, the Examiner withdrew claims 1-4 and 10-13 from consideration. In the drawings, Figures 7-8b have been amended. Applicants submit that no new matter has been introduced into the application by these amendments.

Claim Rejections - 35 USC § 112

Claims 5-9 and 14-19 stand rejected under 35 U.S.C. § 112, first paragraph as failing to comply with the enablement requirement. The Examiner stated that the claims contain subject matter not described in the specification as to enable one of ordinary skill in the art to practice the invention without undue experimentation because the specification and claims provide that the current sources are to be controlled by the error-amplified voltage; however, in Figure 7, the error-amplified voltage is connected to the ground making it impossible for this voltage to control the currents sources Ic and Id.

Enclosed herewith is a revised Figure 7, which shows an arrow from the connecting node of the output terminal of the Feedback Loop block (for outputting the $V_{E/A}$ signal) and the resistor R_e to the Ramp Generator block. Fig. 7 has been modified to clarify the present invention. Applicants respectfully submit that its failure to show an arrow from the connecting node of the output terminal of the Feedback Loop block and the resistor R_e to the Ramp Generator block in Fig. 7 of the drawings was inadvertent, but is implicit from the context of the disclosure.

Amended Fig. 7 is supported by the specification. This feature is described in paragraph [0013], which states "An error-amplified voltage $V_{E/A}$ is generated by a feedback loop. And then this $V_{E/A}$ is input into a ramp generator." therefore, the present amendment does not contain new matter. Similarly, Claim 5 discloses a ramp generator coupled to the feedback loop for receiving the error-amplified voltage and being controlled by the error-amplified voltage. Fig. 5 shows another embodiment of the present invention, which includes a similar arrow from the output terminal of the Feedback Loop block to the Synchronous Signal block for inputting the $V_{E/A}$ signal and for outputting a variable frequency synchronous signal according to the $V_{E/A}$ signal. This feature is described in Paragraph [0027], lines 1-7 of the specification such that one of ordinary skill in the art would understand that an arrow in Fig. 7 as aforementioned should be added for the ramp generator to receive and to be controlled by the $V_{E/A}$ signal. Otherwise, the ground, instead of the Ramp Generator block, would

receive the $V_{E/A}$ signal and current sources I_c and I_d and the Ramp Generator block could not be controlled by the $V_{E/A}$ signal.

Applicant respectfully submits that the modified Fig. 7 of the present invention clarifies the contents of the present invention and avoids the mistake that the $V_{E/A}$ signal is received by the ground instead of the Ramp Generator block. The contents of the modified drawings and the specification of the present invention comply with the enablement requirement under 35 U.S.C. 112 and are thus, patentable.

In addition, for clarity, the Feedback Loop, Ramp Generator, and PWM comparator in Fig. 7, and the time correlation lines in Figs. 8, are now generally outlined in phantom.

Conclusion

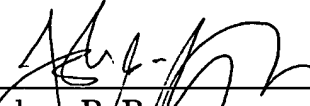
If the Examiner believes that any additional minor formal matters need to be addressed in order to place this application in condition for allowance, or that a telephone interview will help to materially advance the prosecution of this application, the Examiner is invited to contact the undersigned at the Examiner's convenience.

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Application No.: 10/656,420

In view of the foregoing amendments and remarks, Applicants respectfully request reconsideration, approval of the drawing amendment, and withdrawal of the rejection under 35 U.S.C. 112, first paragraph and submit that the present application, including claims 1-19, is in condition for allowance and a notice to that effect is respectfully requested.

Respectfully submitted,

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